SAMPLE RESUME

✉ 888 Debbie Place ▪ Tehachapi, California 93561
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QUALIFICATIONS PROFILE

Highly accomplished and proficient Electronic Engineer, offering progressive years of experience in electronic circuit design and automated testing. Exemplary keen expertise in testing electronic circuits and systems equipped with broad technical experience in RF, analog, and digital technology. Strong background in product development, with outstanding history of managing projects, from conceptualization, through development, all the way through completion. Demonstrated in-depth knowledge in scheduling, resource management, analysis, and troubleshooting. Outstanding ability in identifying and resolving issues, coordinating teams, and reducing costs while increasing overall efficiency. Recognized for attention to detail, adherence to standards, coupled with impressive technical background.

AREAS OF EXPERTISE

- ✓ Electronic Circuits and Systems Testing
- ✓ Process and Procedure Improvement
- ✓ Leadership and Teambuilding
- ✓ Time and Budget Management
- ✓ Problem Resolution and Decision Making
- ✓ Articulate Oral and Written Communication
- ✓ Electronic Engineering
- ✓ Hardware and Software Development
- ✓ Project Management and Operations
- ✓ Superior Interpersonal / Presentation Skills
- ✓ Highly Organized with Strong Analytical Abilities
- ✓ Able to Multitask in Fast-Paced Environments

EDUCATION

BACHELOR OF SCIENCE, ELECTRICAL ENGINEERING
Graduated cum laude, **GPA: 3.58 OUT OF 4.0**
University of Texas at Dallas, Richardson, TX: 1997–2005

CAREER HIGHLIGHTS

- Conceptualized and innovated automated test systems that replace operator performed manual tests
- Reduced test time while enhancing product quality through elimination of operator error
- Incorporated GENRAD-style product fixtures into automated functional test systems increasing flexibility and facilitating production scheduling
- Created various electronic circuits including Product fixtures for automated test systems, PC card with parallel digital I/O bus, Synchronous and Asynchronous RS232/422/485, more than a dozen manual testers, and FPGA, CPLD, PAL, and EPLD loads utilizing Xilinx and Altera tools
- Examined new products to identify test specifications and methods
- Collaborated with R&D to resolve design for test and other production issues
- Ensured compliance of test department projects with schedule and design standards
- Improved RMA Department turnaround performance from four-six weeks to less than two weeks
- Key member of cross-functional continuous improvement team
- Functioned as technical expert for team migrating production from Calgary, Alberta to Dallas
- Played a crucial role in company's initial ISO9001 certification
- Recommended capital equipment purchases exceeding $60,000 for annual budgets

EMPLOYMENT HISTORY

ELECTRONIC ENGINEER (GS-0855), FLIGHT INSTRUMENTATION ENGINEER **2007–PRESENT**
NASA DRYDEN FLIGHT RESEARCH CENTER ■ EDWARDS, CA

TEACHING ASSISTANT **2006**
UNIVERSITY OF TEXAS AT DALLAS ■ RICHARDSON, TX

CONTRACT TEST ENGINEER **2001**
ENGINEERING TEST SUPERVISOR **1989–1998**
WESTRONIC ■ RICHARDSON, TX

INFORMATION ANALYST **1998–2001**
EDS ■ PLANO, TX

TEST ENGINEER **1998**
ODS NETWORKS ■ RICHARDSON, TX

ELECTIVE COURSES AND PROJECTS

- **Analog IC Design**: BJT analog ICs and systems
- **CMOS Analog IC Design (Graduate Level):** Introduction to CMOS technology and analog circuit modeling
- **Semester Project:** Created and simulated with TI Spice, a CMOS op amp with capability of driving a 32-ohm load over the audio band, utilizing 3.3V supply
- **VHDL Modeling (Graduate Level)**: Introduction to VHDL tools and methodologies for synthesis of custom logic
- **Semester Project:** Designed, synthesized, and simulated an IEEE 802.3 Ethernet MAC using VHDL and Synopsys tools
- **DSP-based Senior Design Project**: Designed a QAM voiceband modem in MatLab, verified and validated operation, and executed on a TI '6711 DSP

PROFESSIONAL TRAINING

- Introduction to Aeronautics
- ITC Onboard Solid State Recording Standard Seminar, IRIG 106, Chapter 10
- UML in Object-oriented Design
- Team Building for Managers